



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TAKAHASHI, Koji**

Group Art Unit: **2823**

Serial No.: **10/649,994**

Examiner: **KEBEDE, Brook**

Filed: **August 28, 2003**

P.T.O. Confirmation No.: 6035

**FOR: SEMICONDUCTOR MEMORY CAPABLE OF BEING DRIVEN AT LOW  
VOLTAGE AND ITS MANUFACTURE METHOD**

**RESPONSE TO ELECTION OF SPECIES REQUIREMENT**  
**DATED OCTOBER 5, 2004**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: October 20, 2004

Sir:

In response to the Requirement for an Election of Species Requirement dated **October 5, 2004**, the applicant provisionally elects to prosecute the species embodying Species I, drawn to the first embodiment, method of manufacturing a semiconductor device (i.e., wherein the method comprises forming of two layers of gate insulating film). Claims 8-11 read on this species. The applicant reserves the right to file a divisional application on the non-elected claims.

The applicant respectfully requests an early examination on the elected claims 8-11 and favorable action on the merits. If the Examiner has any questions with this Response, please feel free to call the undersigned at the indicated telephone number.

In the event that this paper is not timely filed, applicant hereby petitions for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

In the event any additional fees are required in connection with this response, please  
charge our Deposit Account No. 01-2340.

Respectfully submitted,

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